



## Rutuja S.

Associate -TA at Synapse Design Inc.

Bengaluru, Karnataka, India

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**SYNAPSE TECHNO DESIGN  
INNOVATIONS PRIVATE LIMITED**



**Marathwada Mitra Mandal's  
Institute of Technology, Lohgao...**

## Activity

**NVIDIA number one in 2023 Nvidia will likely become the largest semiconductor company for the year 2023. We at Semiconductor Intelligence (SC-IQ)...**



**Greetings from SYNAPSE DESIGN A QUEST GLOBAL COMPANY Job Opening for ASIC/IP/SOC Design Verification Engineer (5+ Years Experience) Excited to...**

Liked by Rutuja S.

**Quest Global is hiring VLSI Engineers at various levels for Turnkey / ODC Programs 1. RTL Design with PCIe / DDR / CXL (6+ YEarS) 2. Design...**

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## Experience



### Talent Acquisition Associate

Feb 2023 - Present · 8 months

Bengaluru, Karnataka, India

### Talent Acquisition Trainee

Jun 2022 - Mar 2023 · 10 months

Bengaluru, Karnataka, India

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## Education



Bachelor of Engineering - BE · Mechanical Engineering

2016 - 2021

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## More activity by Rutuja

**Dear Friends, Wish you and your dear ones a Happy Ganesh Chaturthi!  
#togetherwearestronger #thankyou #gratitude #progress #bliss**

Liked by Rutuja S.



**<https://lnkd.in/diRecde5> #ocicertified I'm happy to share that with you I completed my first OCI #certification certification #oracle**

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**Greetings from Synapse Design A Quest Global Company We are Hiring RTL Design Leads/Technical leads/Architects Experience - 8 to 12 years Location...**

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**Greetings from Synapse Design - A Quest global company!!! We are Hiring IP/SOC Verification engineers. Experience : 4.5 to 15 Years Required Skills...**

Shared by Rutuja S.

**Dear Chip Designers, . . . Your Semiconductor Success: Take the Leap and Elevate Your Career Ready for a brighter future in the semiconductor...**

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**Greetings from Synapse Design - A Quest global company!!! We are Hiring IP/SOC Verification engineers. Experience : 4.5 to 15 Years Required...**

Liked by Rutuja S.

**The Art of Verification**

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**Dear Chip Designers, . . . Exponential :-> The semiconductor industry has experienced exponential growth in recent years, driven by increasing demand...**

Liked by Rutuja S.

**With 65% of professionals admitting that work-related stress has risen, it's crucial to acknowledge the toll it takes on productivity and well-being....**

Liked by Rutuja S.



## Virtual Sequences: The Key to Scalable and Reusable Verification As an ASIC verification...

Liked by Rutuja S.

## The Art of Verification

Liked by Rutuja S.

## Please Grab this opportunity at Bangalore !!

Liked by Rutuja S.

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**Anuradha R**

Assistant Manager -Talent Acquisition at Quest Global

Bengaluru

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**viswanath R**

HR Manager at SYNAPSE TECHNO DESIGN INNOVATIONS PRIVATE LIMITED

Bengaluru

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**Sri Venkat**

Officer - Talent Acquisition

Bengaluru

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**Aishwrya Kashyap**

Looking for Senior Leader in Design Verification, RTL Design, & DFT. I have something very exciting Opportunity those are ready to lead the Larger Programs - We can connect

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**nayankumar navadiya**

Experienced semiconductor professional(ASIC/SoC/IP)

Santa Clara, CA

Connect

**Varsha Tayade**

Executive – Talent Acquisition

Bengaluru





...ment from Bangalore...

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India

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### Shivaranjini Mahesh

Finding top talents in PD, DV & DFT to support Adeptchips's growth and continued leadership in Semiconductor

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